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**/ MICROPROCESSOR COMPONENTS /**

1. FPGA means
   1. Field Programmable Gate Array
   2. Forward Programmable Gate Array
   3. Forward Parallel Gate Array
   4. Field Parallel Gate Array

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1. Which language could be used for programming an FPGA.
   1. Verilog
   2. VHDL
   3. Both A and B
   4. None of the above

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1. The program counter in a 8085 micro-processor is a 16-bit register, because
   1. It counts 16-bits at a time
   2. There are 16 address lines
   3. It facilitates the user storing 16-bit data temporarily
   4. It has to fetch two 8-bit data at a time

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**Answer:** (b).

There are 16 address lines

1. A microprocessor is ALU
   1. and control unit on a single chip.
   2. and memory on a single chip.
   3. register unit and I/O device on a single chip.
   4. register unit and control unit on a single chip.

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**Answer:** (d).

register unit and control unit on a single chip.

**5.** Basic steps of execution of an instruction is

**a.** fetch → execute → decode

**b.** decode → fetch → execute

**c.** execute → fetch → decode

**d.** fetch → decode → execute

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**Answer:** (d).

fetch → decode → execute

1. A microprocessor with a 12-bit address bus will be able to access
   1. 1 K bytes
   2. 4 K bytes
   3. 8 K bytes
   4. 10 K bytes

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**Answer:** (b). 4 K bytes

**7.** DMA is used between

**a.** microprocessor and I/O

**b.** microprocessor and memory

**c.** memory and I/O

**d.** none

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**Answer:** (c). memory and I/O

1. Which of the data transfer is not possible in microprocessor
   1. memory to accumulator
   2. accumulator to memory
   3. memory to memory
   4. I/O device to accumulator

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**Answer:** (c). memory to memory

**9.** Which one of the following statement is false?

**a.** A microprocessor has bi-directional address bus

**b.** A microprocessor has unidirectional address bus

**c.** A microprocessor has bi-directional data bus

**d.** A microprocessor has an ALU

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**Answer:** (a).

A microprocessor has bi-directional address bus

1. In microprocessor based system DMA refers to
   1. direct memory access for microprocessor
   2. direct memory access for the user
   3. direct memory access for the I/O device
   4. none of the above

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**Answer:** (c).

direct memory access for the I/O device

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